

2SK0615 (2SK615)

Silicon N-Channel MOS FET

For switching

■ Features

- Low ON-resistance
- High-speed switching
- Allowing to be driven directly by CMOS and TTL
- M type package, allowing easy automatic and manual insertion as well as stand-alone fixing to the printed circuit board.

■ Absolute Maximum Ratings (T_a = 25°C)

Parameter	Symbol	Ratings	Unit
Drain to Source voltage	V _{DS}	80	V
Gate to Source voltage	V _{GSO}	20	V
Drain current	I _D	±0.5	A
Max drain current	I _{DP}	±1	A
Allowable power dissipation	P _D *	1	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

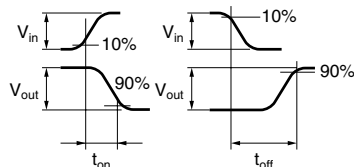
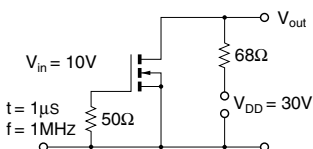
* PC board: Copper foil of the drain portion should have an area of 1cm² or more and the board thickness should be 1.7mm.

■ Electrical Characteristics (T_a = 25°C)

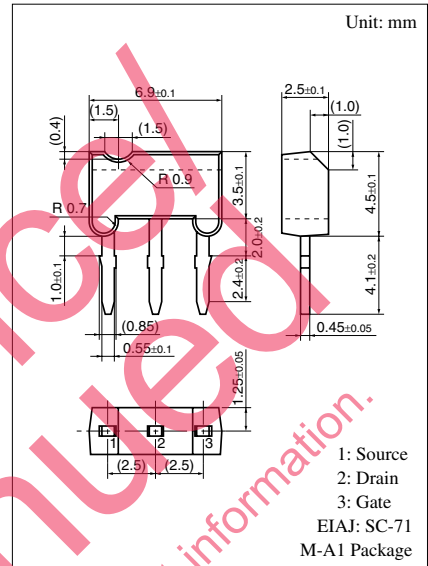
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source cut-off current	I _{DSS}	V _{DS} = 60V, V _{GS} = 0			10	μA
Gate to Source leakage current	I _{GSS}	V _{GS} = 20V, V _{DS} = 0			0.1	μA
Drain to Source breakdown voltage	V _{DSS}	I _D = 100μA, V _{GS} = 0	80			V
Gate threshold voltage	V _{th}	I _D = 1mA, V _{DS} = V _{GS}	1.5		3.5	V
Drain to Source ON-resistance	R _{DS(on)} ^{*1}	I _D = 0.5A, V _{GS} = 10V		2	4	Ω
Forward transfer admittance	Y _{fs}	I _D = 0.2A, V _{DS} = 15V, f = 1kHz		300		mS
Input capacitance (Common Source)	C _{iss}	V _{DS} = 10V, V _{GS} = 0, f = 1MHz		45		pF
Output capacitance (Common Source)	C _{oss}			30		pF
Reverse transfer capacitance (Common Source)	C _{rss}			8		pF
Turn-on time	t _{on} ^{*1, 2}			15		ns
Turn-off time	t _{off} ^{*1, 2}			20		ns

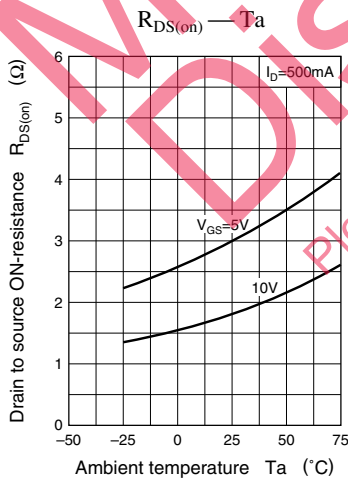
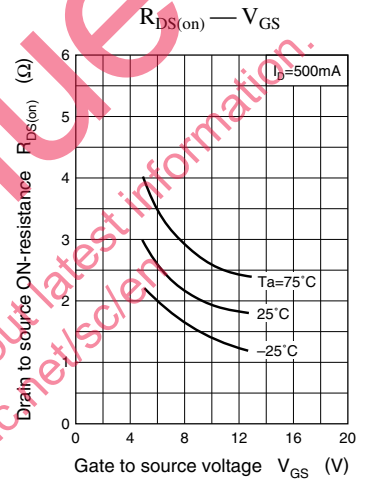
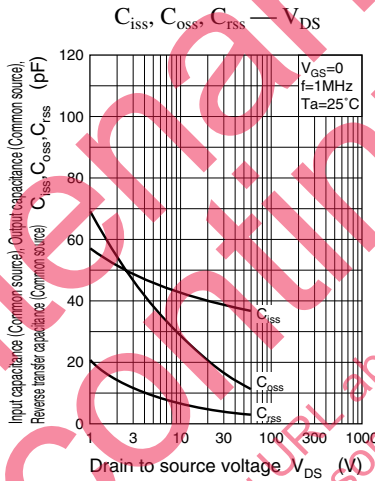
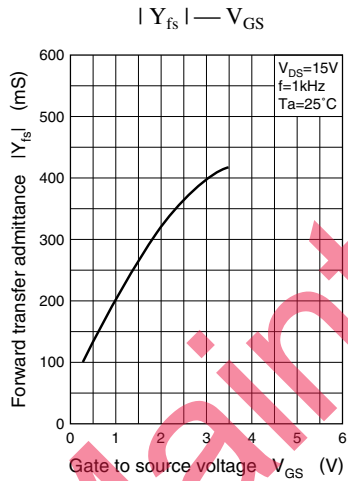
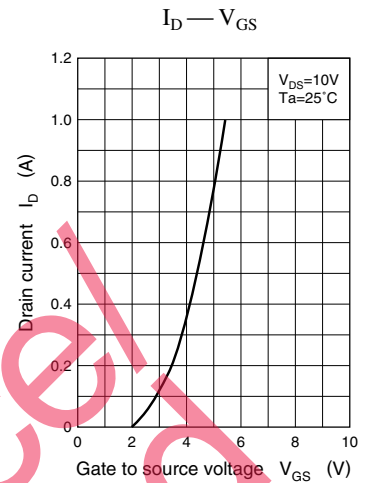
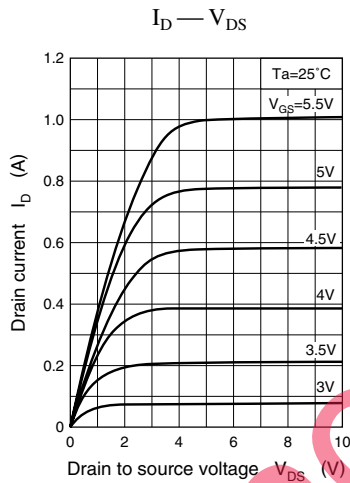
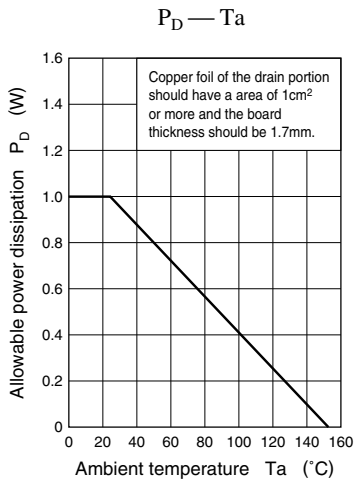
*1 Pulse measurement

*2 t_{on}, t_{off} measurement circuit



Note) The part number in the parenthesis shows conventional part number.





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